

10/17/2001

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10020314	12/18/2001	438		2823	NGUYEN, K

APPLICANT(S): Shimamoto Yacuhiro, Hiratani Masahiko, Matsui Yuichi; Yamamoto Satoru, Nabatame Toshihide, Ando Toshio, Sakuma Hiroshi, Iijima Shinpei;

CONTINUING DATA VERIFIED:

FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-024389 01/31/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	HITA.0143
Verified and Acknowledged Examiner's initials		
TITLE : Fabricating method of semiconductor integrated circuits		
U.S. DEPT. OF COMM/PAT & TM-PTO-450 (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claims Avg 0.0
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Fig.s Drawn
		Print Fig.	
TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Section 122, 181 and 365. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

 DISK (CRF) CD-ROM

(Attached in pocket on right inside flap)